53 GBd PAM-4 DAC-LESS LOW-POWER (1.5 pJ/b) SILICON INTEGRATED TRANSMITTER

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Abstract

We present an integrated PAM-4 transmitter consisting of two parallel GeSi electro-absorption modulators wirebonded to a SiGe BiCMOS driver, consuming 160mW excluding laser. 53GBd PAM-4 transmission is demonstrated over more than 1km fiber and 2km non-zero dispersion-shifted fiber, without using equalization or digital signal processing.

1 Introduction

Recent 400 Gigabit Ethernet (GbE) standards, e.g. 400GBASE-DR4, specify 53.125 GBd PAM-4 per lane up to 500m standard single-mode fiber (SSMF) for datacenter optical interconnects [1]. Longer fiber reaches up to 10 km are proposed in e.g. the 400GBASE-FR4 specifications [2]. These applications require high-bandwidth, linear PAM-4 transceivers in a very compact form factor and with low power consumption. Several PAM-4 transmitters have been demonstrated at or above 50 GBd [3-12]. Furthermore ref. [5] demonstrates a real-time 56 GBd PAM-4 link up to 2 km SSMF, but at 8.6 W power consumption (excluding laser). However, [3-5] are based on long, travelling-wave Mach-Zehnder modulators (MZMs), which typically occupy a lot of area (several mm²) in the transceiver, even when monolithically integrated with the driver [13]. These travelling-wave structures typically require 50Ω terminations in the driver(s) and on the PIC, increasing the TX power consumption considerably. More compact single-modulator solutions, e.g. electro-absorption modulated lasers (EAM-EMLs)[6], and directly modulated lasers (DMLs)[7] often rely on power-hungry electrical digital-to-analog converters (DACs) to produce a (predistorted) PAM-4 signal which has to be linearly converted into the optical domain by the modulator. These transmitters typically use significant digital signal processing (DSP) at the TX or RX, or both. In [8], 56 GBd PAM-4 was transmitted with a single electro-absorption modulator (EAM) driven by an electrical DAC and RF-amplifier while transmitting (TX) and receiver (RX) DSP, resulting in bit error ratios (BERs) below the HD-FEC threshold (3.8x10^-3) up to 2 km SSMF. In [9], a single microring modulator (MRR) and an integrated laser are combined with a low-power (160 mW) CMOS driver with on-chip compensation for the MRR characteristics. This resulted in 56 GBd PAM-4 with a very good eye quality, however without fiber transmission or BER. Out of [3-14], only [10], [11-12] and [14] utilize PAM-4 generation in the optical domain (optical DAC). In [10], up to 64 GBd PAM-4 is demonstrated with a segmented microring resonator (MRR), without driver. In [14], a low-power transmitter is demonstrated based on flip-chip integration of a CMOS driver on a segmented MZM, but it is limited to 28 GBd. In [12], we demonstrated 64 GBd PAM-4 transmission over 1 km SSMF in real-time, using a parallel configuration of NRZ-driven EAMs. However a combination of RF-probes, an analog TX FFE (850 mW) and RF-amplifiers (5W) was used to drive the EAMs, due to the lack of an EAM driver IC. In this paper, a photonic integrated circuit (PIC) from imec’s iSiPP50G platform with parallel EAMs incorporated in a Mach-Zehnder interferometer (MZI) is wirebonded to a very low-power EAM driver IC [15], implemented in 55 nm SiGe BiCMOS. The PIC contains a tunable optical power splitter and two lumped, unterminated EAMs, as was proposed in [11] but not yet implemented in [12]. The linearity requirements are removed from both driver and modulator, resulting in a low TX power consumption of only 160 mW (excluding laser), including the DC power dissipated in the heaters on the PIC (less than 15 mW). The EAMs are very compact, each EAM occupying only 120x180 µm² including metal routing and bondpads. BERs below or close to the KP4-FEC-threshold (2.4x10^-4) are shown for transmission of 53 GBd PAM-4 at 1565 nm across more than 1 km of SSMF and 2 km of non-zero dispersion-shifted fiber (NZ-DSF), while consuming only 1.5 pJ/b and occupying 0.275x1.72 mm² on the PIC and 0.75x1.2 mm² on the EIC.

2 Experiment setup

The measurement setup is illustrated in Fig. 1. The operation principle of the transmitter, described in detail in [11] and [12], is shown in Fig. 2. Two PRBS 2^5-1 pseudo-random bit sequences (PRBS) at 53 Gb/s, produced by a 92 GS/s arbitrary waveform generator (AWG) and decorrelated by introducing a relative delay (of at least 20 symbol periods) in the AWG, are sent directly through a 6-inch multi-coax cable
assembly and RF-PCB transmission lines to the differential inputs of two driver channels in the TX electrical IC (EIC). The TX EIC non-linearly amplifies the input signals and drives the EAMs with up to 2 Vpp differential swing. The wirebond length towards the EAMs is minimized in the assembly to improve bandwidth and reduce bondwire-induced peaking (see Fig. 2). The external laser, set to 12 dBm at 1565 nm, is coupled into and out of the PIC through fiber-to-chip grating couplers (FGCs, 6 dB loss/FGC). Coupling loss can be improved to 2 dB per coupler with edge couplers [15]. A 50/50 power splitter is used at the input, ensuring that the EAMs receive equal optical power and behave as identical loads towards the driver IC. One branch is phase-shifted ($\Delta\phi$ in Fig. 1-2) w.r.t. the other branch with a thermo-optic phase shifter. By default, the driver EIC biases both EAMs identically at approx. -1V, resulting in approximately 1.5mA DC photocurrent per EAM. The insertion loss per EAM was approximately 5 dB. Next, the modulated light is recombined using a tunable power combiner (implemented as a balanced MZI). Nominally the MZI is set to 33/66-operation ($\alpha=1/3$, Fig. 1-2), theoretically resulting in equidistant PAM-4 after power detection by the photodetector (PD) (Fig. 2) [11]. Thanks to the tunable optical recombination, up to 1.25 dB OMA improvement is expected w.r.t. an electrically weighted equivalent, as shown in [12]. After transmission over SSMF, an Erbium-doped fiber amplifier (EDFA) is used to overcome the insertion loss of the FGCs and as part of the reference receiver, which further consists of a variable optical attenuator (VOA) and a 70 GHz PD. The PD output is captured by a digital sampling oscilloscope (DSO) for eye diagrams, or by a 63 GHz 160 GSa/s real-time oscilloscope (RTO) for offline BER measurements. BER is measured directly after resampling, without using DSP.

3 Results

In Fig. 4, the TX eyes diagrams, measured with the reference RX, are plotted vs. data rate. The TX outer extinction ratio

![Fig. 4. TX output at (a) 28GBd (12ps/div, 15mV/div); (b) 40GBd (8ps/div, 15mV/div) and (c) 53GBd (8 ps/div, 16 mV/div).](image)

![Fig. 5. TX NRZ outputs at 53 Gb/s. (a) LSB, 10 mV/div, 8 ps/div (b) MSB, 12 mV/div, 8 ps/div.](image)
to approx. 1.25 km. Only a minimal BER penalty is observed in the back-to-back case: at 53 GBd, a back-to-back BER of $3.77 \times 10^{-6}$ is obtained with predistortion vs. a BER of $3.17 \times 10^{-6}$ for the equidistant case. However without MZI predistortion, the BER increases above the KP4 threshold beyond 500m, whereas with predistortion, the BER at 1 km SSMF is still $8.3 \times 10^{-5}$. With 2 km SSMF, a BER of $4.1 \times 10^{-3}$ is measured, just above the HD-FEC threshold ($3.8 \times 10^{-3}$). With 2 km NZ-DSF (single point), the BER is $2.62 \times 10^{-4}$, just above KP4-FEC. At 40 GBd, the back-to-back TX BER (unpredistorted) decreases to $1.1 \times 10^{-6}$, but the BER increase vs. fiber length is similar to the unpredistorted 53 GBd case. Except for the 40 GBd back-to-back case ($1.1 \times 10^{-6}$), the BER at 53 GBd with predistortion is better than at 40 GBd without predistortion. For 28 GBd with predistortion, the back-to-back BER ($1.43 \times 10^{-6}$) is slightly higher than the BER minimum of $7.35 \times 10^{-7}$ at 500m, due to the predistortion which results in a smaller center eye. For 28 GBd, the BER is below KP4-FEC beyond 2 km SSMF ($1.2 \times 10^{-5}$), and below HD-FEC up to 3.5 km SSMF ($1.9 \times 10^{-5}$). Table 1 summarizes the state of the art on integrated PAM-4 transmitters. Compared to single-modulator solutions, the TX generates PAM-4 optically from two NRZ-driven EAMs. Therefore no linear drivers are required and the non-linearity of the modulator does not need to be compensated (as in [17]), which can reduce the power consumption of the TX significantly. Compared to classical MZM-based transmitters [13], even with very compact MZMs (~750μm length in [14]), the TX is very low-power and compact, occupying only 0.275x1.72 mm² on the PIC.

### Table 1. State-of-the-art integrated PAM-4 transmitters

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<td>[9]</td>
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*At 25 GBd, 28 GBd BER > KP4-FEC due to RX.
**Excluding heater(s) or temp. control. ***Including heaters

### Conclusion

We have presented a 53 GBd silicon integrated PAM-4 optical transmitter, capable of sub-KP4-FEC BERs over up to more than 1 km SSMF at 1565 nm and a BER just above KP4-FEC with 2 km NZ-DSF, without using any equalization or DSO. Compared to the state of the art, the transmitter is both very compact and low-power (1.5 pJ/b).

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6 References


